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Personal Information

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International Researcher IDs

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Education

Doctorate, Istanbul Technical University, Fen Bilimleri Enstitüsü, Turkey 2014 - 2020

Postgraduate, Istanbul Technical University, Fen Bilimleri Enstitüsü, Turkey 2012 - 2014

Undergraduate, Ataturk University, Mühendislik Fakültesi, Turkey 2005 - 2009

Research Areas

Electronic, Printed Circuits, Thin Film, Thick Film and Hybrid ICs, Electronic Circuits, Nanotechnology

Academic Positions

Assistant Professor, Ataturk University, Mühendislik Fakültesi, Elektrik-Elektronik Mühendisliği, 2022 - Continues

Research Assistant, Istanbul Technical University, 2012 - 2020

Journal articles indexed in SCI, SSCI, and AHCI

I. Emulation of a constant phase element by utilizing a lattice structure based fractional-order differentiator

Rezazadehshabilouyoliya V., Atasoyu M., Ozoguz S.

AEU-INTERNATIONAL JOURNAL OF ELECTRONICS AND COMMUNICATIONS, vol.127, 2020 (SCI-Expanded)

II. Sensing schemes for STT-MRAMs structured with high TMR in low RA MTJs

Atasoyu M., Altun M., Özoguz İ. S.

MICROELECTRONICS JOURNAL, vol.89, pp.30-36, 2019 (SCI-Expanded)

III. Simple realization of a third order Butterworth filter with MOS-only technique

Atasoyu M., Metin B., Kuntman H., Cicekoglu O.

AEU-INTERNATIONAL JOURNAL OF ELECTRONICS AND COMMUNICATIONS, vol.81, pp.205-208, 2017 (SCI-Expanded)

IV. New Current-Mode Class 1 Frequency-Agile Filter for Multi Protocol GPS Application

Atasoyu M., Metin B., Kuntman H., Herencsar N.

ELEKTRONIKA IR ELEKTROTECHNIKA, vol.21, no.5, pp.35-39, 2015 (SCI-Expanded)

Articles Published in Other Journals

I. THE COGNITIVE MODELS USING OSCILLATORY NEURAL NETS

ATASOYU M., ÖZOĞUZ İ. S.

The Journal of Cognitive Systems, 2021 (Peer-Reviewed Journal)

Papers Presented at Peer-Reviewed Scientific Conferences

I. Inverter based OTA Design Using gm/ID Transistor Sizing Technique

ATASOYU M.

2021 29th Signal Processing and Communications Applications Conference (SIU), İstanbul, Turkey, 09 June 2021, (Full Text)

II. Inversion Coefficient Design Methodology for the Design of Inverter based OTA

ATASOYU M.

INTERNATIONAL SYMPOSIUM ON APPLIED SCIENCES AND ENGINEERING, Erzurum, Turkey, 7 - 09 April 2021, (Full Text)

III. Inverter based OTA Design for Second-Order Gm-C filters

ATASOYU M.

INTERNATIONAL SYMPOSIUM ON APPLIED SCIENCES AND ENGINEERING, Erzurum, Turkey, 7 - 09 April 2021, (Full Text)

IV. A Tunable Immittance Simulator with a Voltage Differential Current Conveyor

METİN B., ATASOYU M., ARSLAN E., Herencsar N., ÇİÇEKOĞLU M. O.

60th International Midwest Symposium on Circuits and Systems, 6 - 09 August 2017, (Full Text)

V. Design of a power output unit for HF transmitters

Osman P., ÖZOĞUZ İ. S., Aydin O., ATASOYU M.

2017 40th International Conference on Telecommunications and Signal Processing (TSP), Barcelona, Spain, 5 - 07 July 2017, (Full Text)

VI. Spin-torque memristor based offset cancellation technique for sense amplifiers

ATASOYU M., ALTUN M., ÖZOĞUZ İ. S., Roy K.

2017 14th International Conference on Synthesis, Modeling, Analysis and Simulation Methods and Applications to Circuit Design (SMACD), Giardini Naxos, Italy, 12 - 15 June 2017, (Full Text)

VII. Design of current mode class 1 frequency agile filter employing CDTAs

ATASOYU M., METİN B., KUNTMAN H. H., ÇİÇEKOĞLU M. O.

2015 European Conference on Circuit Theory and Design (ECCTD), Trondheim, Norway, 24 - 26 August 2015, (Full Text)

VIII. Frequency agile filter structure improved by MOS only technique

ALAYBEYOĞLU E., ATASOYU M., KUNTMAN H. H.

2015 38th International Conference on Telecommunications and Signal Processing (TSP), Prague, Czech Republic, 9 - 11 July 2015, (Full Text)

IX. GPS Uygulamaları için OTA Tabanlı Frekans Atık Sürgeç Yapısı

ATASOYU M.

ELECO 2014, Turkey, 27 - 28 November 2013

X. Reconfigurable filter structure designed for different positioning systems

ATASOYU M.

2014 International Conference on Applied Electronics, Pilsen, Czech Republic, 9 - 10 September 2014

XI. Farklı GPS protokollerİ İçin Düşük Güçlü Atık Sürgeç Uygulaması

ATASOYU M.

2014 22nd Signal Processing and Communications Applications Conference (SIU), Trabzon, Turkey, Turkey, 23 - 25 April 2014, pp.630-633, (Full Text)

Funded Projects

Altun M., H2020 Project, Synthesis and Performance Optimization of a Switching Nano-Crossbar Computer, 2015 - 2019

Metrics

Publication: 16

Citation (WoS): 33

Citation (Scopus): 61

H-Index (WoS): 5

H-Index (Scopus): 6